L Number	Hits	Search Text	DB	Time stamp
7	2	("20030046492").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/28 16:25
10	2562	((711/118,170,173).CCLS.) and @ad < "20010828"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/28 17:16
11	37	((((711/118,170,173).CCLS.) and @ad < "20010828") and (configur$5 with memory with cache)) and (configurable with memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/28 17:17
12	90	((configurable near5 memory near5 cache) and @ad <20010828) and (chip IC (integrated adj circuit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/28 17:17
13	43	((configur$5 with memory) same (ram)) and (macro adj cell) and @ad < "20010828"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/28 17:18
14	80	((chip adj stack) and (integrated adj circuit) and @ad < "20010828") and (flip adj chip)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/10/28 17:19
-	2	("20030046492").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 09:05
-	2909	(711/118,170,173).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 09:06
-	336	(((711/118,170,173).CCLS.) and @ad < "20010828") and (configur$5 with memory with cache)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 09:11
-	239	configurable with memory with cache	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 09:42
-	150	(configurable with memory with cache) and @ad < "20010828"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 09:41
-	108	((configurable with memory with cache) and @ad < "20010828") and (chip IC (integrated adj circuit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 09:42
-	104	(configurable near5 memory near5 cache) and @ad <20010828	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 09:48
-	125	((configurable reconfigurable) near5 memory near5 cache) and @ad <20010828	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 09:48

-	89	(((configurable reconfigurable) near5 memory near5 cache) and @ad <20010828) and (chip IC (integrated adj circuit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 09:48
-	17	(US-6697916-$ or US-6606686-$ or US-6606684-$ or US-6484237-$ or US-6446181-$ or US-6427190-$ or US-6321318-$ or US-5966734-$ or US-6678790-$ or US-6347346-$ or US-6150839-$).did. or (US-20020069331-$ or US-20020078268-$ or US-20020073282-$ or US-20020069341-$ or US-20020065867-$ or US-20030046492-$).did.	USPAT; US-PGPUB	2004/04/28 15:53
-	0	("l18andfuse").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 15:53
-	1	((US-6697916-$ or US-6606686-$ or US-6606684-$ or US-6484237-$ or US-6446181-$ or US-6427190-$ or US-6321318-$ or US-5966734-$ or US-6678790-$ or US-6347346-$ or US-6150839-$).did. or (US-20020069331-$ or US-20020078268-$ or US-20020073282-$ or US-20020069341-$ or US-20020065867-$ or US-20030046492-$).did.) and fuse	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 15:54
-	3	((US-6697916-$ or US-6606686-$ or US-6606684-$ or US-6484237-$ or US-6446181-$ or US-6427190-$ or US-6321318-$ or US-5966734-$ or US-6678790-$ or US-6347346-$ or US-6150839-$).did. or (US-20020069331-$ or US-20020078268-$ or US-20020073282-$ or US-20020069341-$ or US-20020065867-$ or US-20030046492-$).did.) and burn	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 15:58
-	608	((memory with configur$5) same fuse) and @ad < "20010828"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 15:59
-	350	((memory with configur$5) with fuse) and @ad < "20010828"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/28 15:59
-	78	(((memory with configur$5) with fuse) and @ad < "20010828") and processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/29 13:52
-	1	("0591539").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/29 09:19
-	0	("9591539").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/29 09:19
-	0	"09591539"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/29 09:20
-	0	"09591537"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/29 09:20

-	33	"09/591537"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/29 09:20
-	9656	((chip adj stack) (flip adj chip)) and (integrated adj circuit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/29 14:05
-	389	(chip adj stack) and (integrated adj circuit) and @ad < "20010828"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/29 14:12
-	34	((chip adj stack) and (integrated adj circuit) and @ad < "20010828") and fuse	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/29 13:55
-	6794	(flip adj chip) and (integrated adj circuit) and @ad < "20010828"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/29 14:06
-	426	((flip adj chip) and (integrated adj circuit) and @ad < "20010828") and fuse	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/29 14:08
-	1464760	(((flip adj chip) and (integrated adj circuit) and @ad < "20010828") and fuse) memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/29 14:08
-	199	(((flip adj chip) and (integrated adj circuit) and @ad < "20010828") and fuse) and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/29 14:11
-	0	((((flip adj chip) and (integrated adj circuit) and @ad < "20010828") and fuse) and memory) and (chip adj stack)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/29 14:11
-	21	(US-6697916-$ or US-6606686-$ or US-6606684-$ or US-6484237-$ or US-6446181-$ or US-6427190-$ or US-6321318-$ or US-5966734-$ or US-6678790-$ or US-6347346-$ or US-6150839-$ or US-6355968-$ or US-6297666-$).did. or (US-20020069331-$ or US-20020078268-$ or US-20020073282-$ or US-20020069341-$ or US-20020065867-$ or US-20030046492-$ or US-20010049174-$ or US-20020074668-$).did.	USPAT; US-PGPUB	2004/04/29 15:12
-	1	((US-6697916-$ or US-6606686-$ or US-6606684-$ or US-6484237-$ or US-6446181-$ or US-6427190-$ or US-6321318-$ or US-5966734-$ or US-6678790-$ or US-6347346-$ or US-6150839-$ or US-6355968-$ or US-6297666-$).did. or (US-20020069331-$ or US-20020078268-$ or US-20020073282-$ or US-20020069341-$ or US-20020065867-$ or US-20030046492-$ or US-20010049174-$ or US-20020074668-$).did.) and ((macro adj cell) macrocell)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/29 15:14
-	0	(configurable with memory) same (ram and cache) same (macro adj cell) and @ad < "20010828"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/29 15:16

-	0	(configurable with memory) same (ram) same (macro adj cell) and @ad < "20010828"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/29 15:16
-	0	((configurable with memory) same (ram and cache)) and (macro adj cell) and @ad < "20010828"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/29 15:16
-	9	((configurable with memory) same (ram)) and (macro adj cell) and @ad < "20010828"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/29 15:25
-	246	(memory) same (ram) and (macro adj cell) and @ad < "20010828"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/29 15:26